

Models
CD-EPILO-700EB/C
Chip information

FileName:
EPI-Lo chip information
REVISION: 1

REVISION DATE: 20/03/2013

Top view

Junction side or P+ side

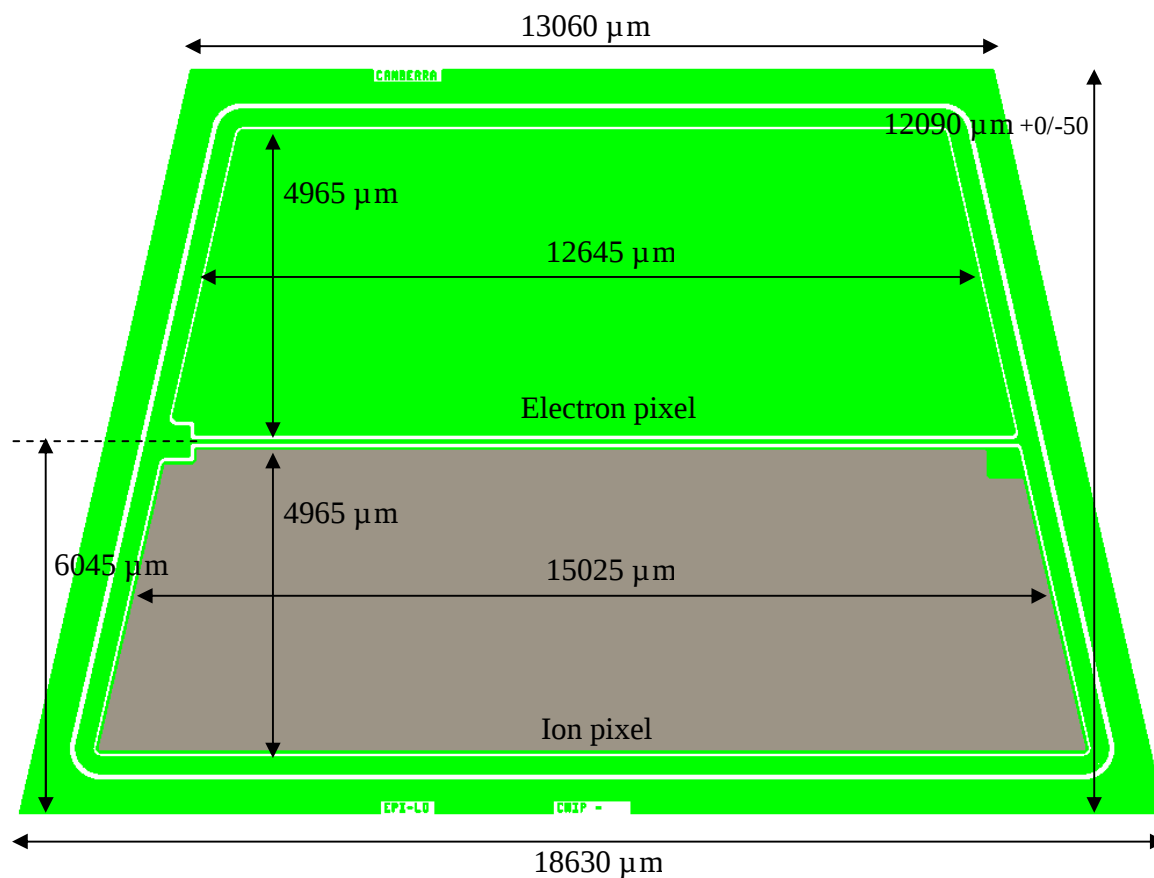


Figure 1: Chip pixel and outer dimensions in μm (top view, junction side or P+ side)

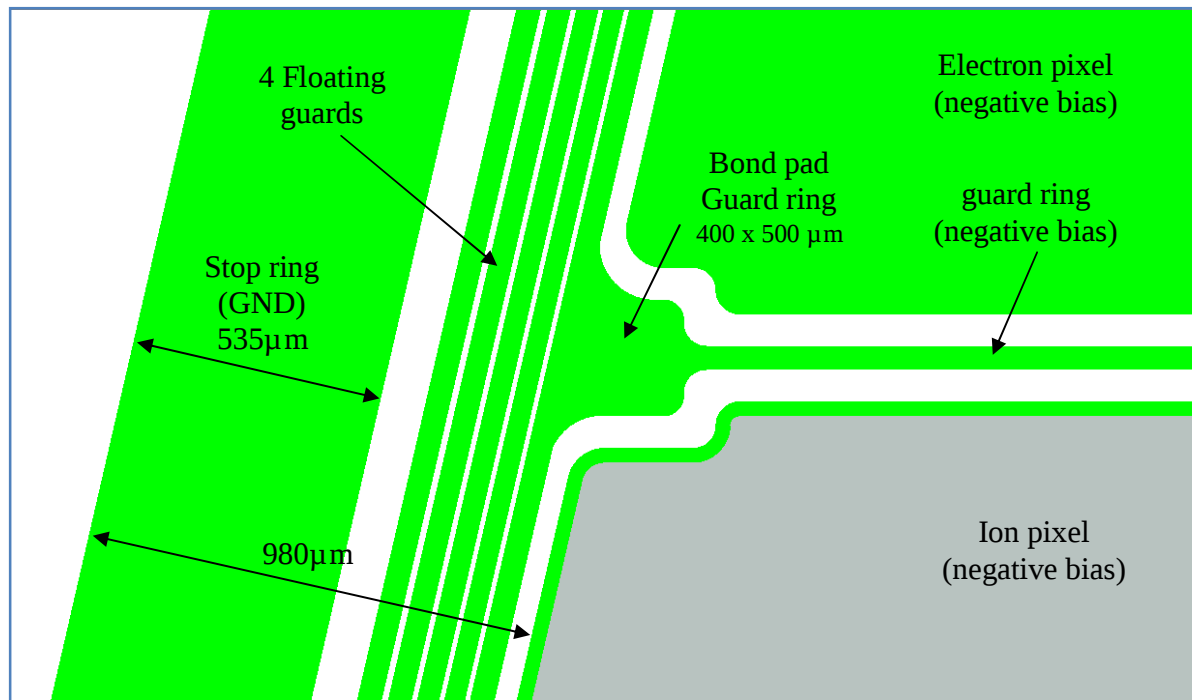


Figure 2: Middle - Left view (top view, junction side or P+ side)

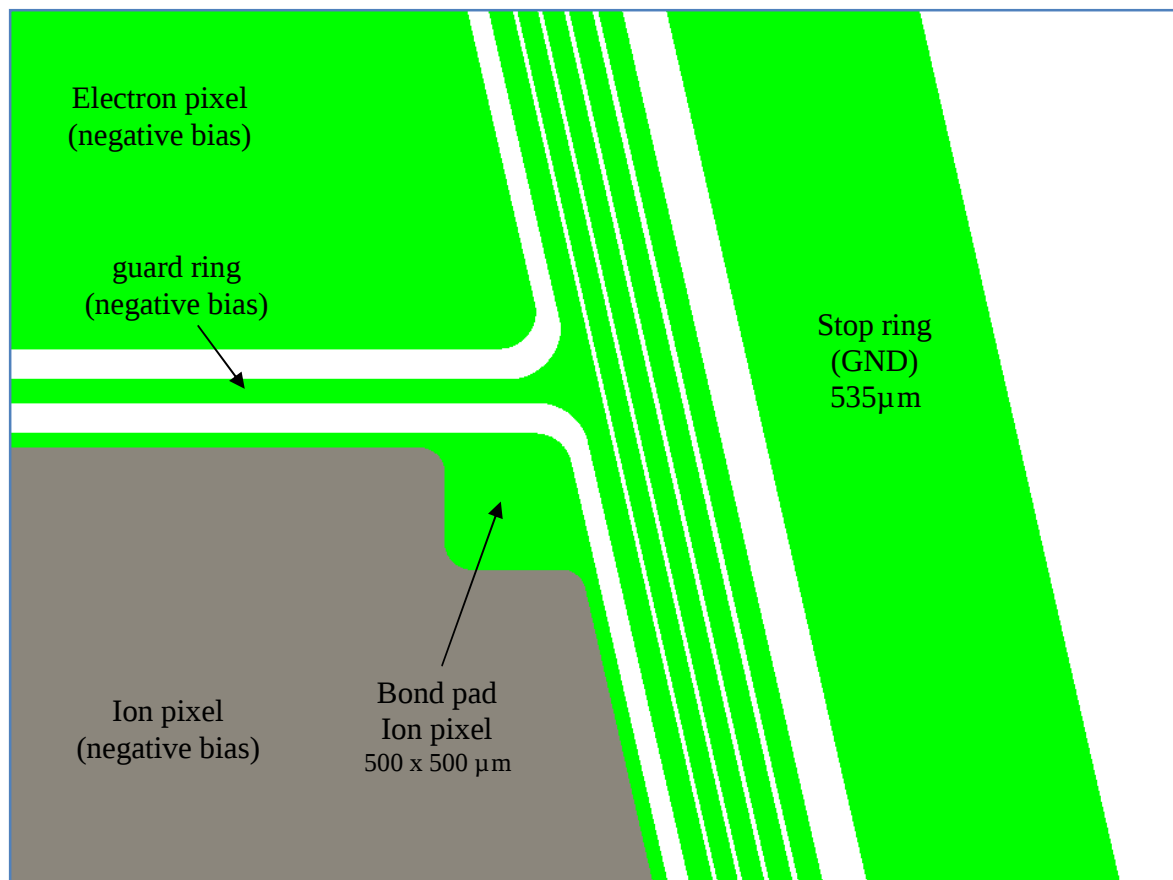


Figure 3: Middle - Right view (top view, junction side or P+ side)

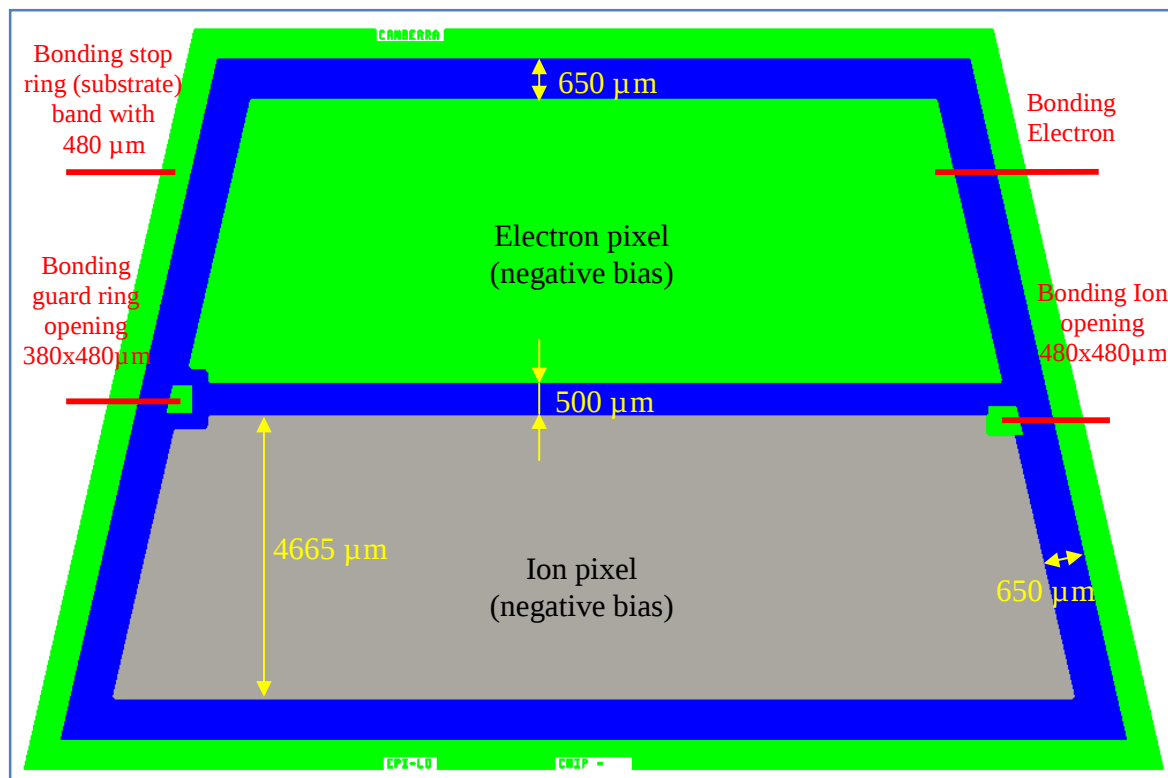


Figure 4: Passivation in blue (top view, junction side or P+ side)
The passivation will overlap the pixels on the sides with 150 μm