FPGA design margins

- MISC operates at 14.8 MHz, same as for NuSTAR. NuSTAR worst case analysis implied operation possible to at least 17.2 MHz.
- Worst case clock margin to be checked using flight-like RTAX 250 and variable clock generator (as on NuSTAR).
- MISC consumes 36% of RTAX 250 “R-cells” and 63% of “C-cells”, leaving 900 R-cells and 1050 C-cells available for application specific use.
- Telescope board FPGA designs are complete.
- Application specific logic consumes: 420 R-cells (47% of available) and 740 C-cells (70% of available)
- Remaining available: 480 R-cells, 310 C-cells.