

EPI_Hi_CBE_power_17.xlsx						EPI-Hi Power Estimate									10/17/2014						
	10/17/2014 0:00	Updated PHASIC channels used. Added column for measured EM DPU + LET1 boards together (JAB)																			
	8/5/2013 17:00	Add min power mode when all PHASIC channels disabled, re-estimate Iq for +5V LDO, Is for +5Vref, and HKchip consumption (BK)																			
	7/16/2013 11:00	Set LVPS outputs to the target values (BK)																			
	6/27/2013 16:00	Updated PHASIC consumption (12mW/ch) (BK)																			
	5/1/2013 14:00	Added redundant S/C I/F driver/receiver, and set LVPS outputs to 0.1V above nominal value (BK)																			
	1/25/2013 17:00	Updated Bias Supply consumption, added 3 op-heater control switches and the combined op-heater power for all 3 telescopes (BK)																			
	11/15/2012 11:00	Reverted rad-hard clock oscillator power to 15mA @ 3.3V with load (in MISC/OSC/SRAM contribution), corrected sourcing of +25V which is from Bias Supply, not LVPS (BK)																			
	11/2/2012 19:30	Updated PHASIC consump (11mW/ch) & individual MISC contrib, added rad-hard clock oscillator delta & Hot Case, deleted references to high rates, corrected some equations (BK)																			
	8/1/2012 10:06	Fixed regulator power, new op-amps added (JAB)																			